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U.S. **UTILITY** Patent Application

PATENT DATE O.I.P.E.

APPLICATION NO. CONT/PRIOR CLASS SUBCLASS ART UNIT **EXAMINER** 389 09/882722 2841 2827 260 APPLICANTS

Robert Clarkson Glen Harding

Printed circuit board having a microelectronic semiconductor device mount area for trace routing therethrough

PTO-2040 12/99

ISSUING CLASSIFICATION											
ORIGINAL CROSS REFERENCE(S)											
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TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED		
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
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WARNING: The information disclosed herein may be rest Possession outside the U.S. Patent & Tradem	tricted. Unauthorized on the control of the control	disclosure may be	prohibited by the U	nited States Code Title 3 ors only.	5, Sections 122, 181 and 368	
orm PTO-436A Rev. 6/99)			FILED WITH:	DISK (CRF)	FICHE CD-RO	

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